

**PATENT APPLICATION**

**A LOW I/O BANDWIDTH METHOD AND SYSTEM FOR  
IMPLEMENTING DETECTION AND IDENTIFICATION OF  
SCRAMBLING CODES**

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## A LOW I/O BANDWIDTH METHOD AND SYSTEM FOR IMPLEMENTING DETECTION AND IDENTIFICATION OF SCRAMBLING CODES

### CROSS-REFERENCES TO RELATED APPLICATION(S)

[01] The present application is related to U.S. Patent Application Serial No. (to be assigned) entitled "A METHOD AND SYSTEM FOR DETECTING AND IDENTIFYING SCRAMBLING CODES," by Sharad Sambhwani et al., commonly owned and filed concurrently herewith, the disclosure of which is hereby incorporated by reference in their entirety as if set forth in full herein for all purposes.

[02] The present application is also related to U.S. patent application serial no. 09/815,122 entitled "ADAPTIVE INTEGRATED CIRCUITRY WITH HETEROGENEOUS AND RECONFIGURABLE MATRICES OF DIVERSE AND ADAPTIVE COMPUTATIONAL UNITS HAVING FIXED, APPLICATION SPECIFIC COMPUTATIONAL ELEMENTS," filed on March 22, 2001, the disclosure of which is hereby incorporated by reference in their entirety as if set forth in full herein for all purposes.

### BACKGROUND OF THE INVENTION

[03] The present invention generally relates to scrambling codes. More specifically, the present invention relates to a method and system for detecting scrambling codes within a W-CDMA communication system.

[04] Code acquisition is a fundamental algorithm required in any direct sequence spread spectrum (DSSS) receiver. Prior to de-spreading, demodulating and decoding frames, such a receiver needs to acquire knowledge of timing information relating to the underlying spreading waveform being used to spread the data-bearing signal. According to the wide-band code division multiple access (W-CDMA) communication system of the 3GPP standards body, upon turning on a mobile terminal or device, a 3-step initial cell search procedure needs to be performed to acquire the primary scrambling code which is used to spread the data bearing channels. Examples of such channels are the primary common pilot channel (P-CPICH) and the dedicated physical channel (DPCH).

[05] The first step of the 3-step initial cell search procedure relates to slot timing. In a W-CDMA communication system, each base station transmits its own scrambling code

in frames over the air to a mobile terminal. Each frame is made up of fifteen (15) slots. Before the start of a frame can be located, the start of a slot needs to be identified first. Once the start of a slot is identified, then it can be assured that one of the next fifteen (15) slots represents the start of a frame. Upon conclusion of the first step, the start of a slot is identified.

[06] The second step of the 3-step initial cell search procedure relates to frame timing. As mentioned above, at the end of the first step, the start of a slot is identified. Once that is achieved, the start of a frame can then be identified. Within a W-CDMA communication system, there are five hundred and twelve (512) base stations within the network. The base stations are identified in the network by a network matrix. The network matrix has sixty-four groups (64) and each group has eight (8) cells. A particular base station is identified by its group and its cell position within the group. During this second step, the start of a frame is identified and the mobile terminal can then synchronize to the identified frame and obtain information relating to group identification. Upon conclusion of the second step, the group which contains the base station that sent out the frame (or scrambling code) is identified, i.e., one out of sixty-four (64) group is identified.

[07] Upon completing the first two steps of the initial cell search procedure, the receiver has knowledge of the slot and frame timing of the received scrambling code, such as a P-CPICH signal. The receiver also has knowledge of the group identification of the base station or cell being acquired. The group identification information contains information on all eight (8) primary cells within the group. Since there are eight (8) cells in a group, using the group identification information, the receiver needs only to identify one (1) out of eight (8) possible primary cells from the group.

[08] To achieve this goal, the receiver may use one of two conventional approaches. Under the first approach, the receiver may perform a correlation of the received signals with a parallel bank of eight (8) scrambling code generators (typical correlation length N ranges from 64 to 256 chips based on frequency offset in the received signals). All the eight (8) correlations are performed within N chips, at the expense of using eight (8) parallel scrambling code generators.

[09] Under the second approach, the receiver may sequentially correlate the received signals with eight (8) possible scrambling codes for N chips each. Using a single scrambling code generator, one may attain all eight (8) correlation results after slightly greater than  $8*N$  chips (this number of chips is needed to allow for reassigning the scrambling code generator to another phase offset, after each correlation is performed),

[10] Implementations may not be limited to the above two conventional approaches. The above two approaches were explained for the case of real time processing of the CDMA signal, i.e. no buffering of received data was assumed for these two cases.

[11] As mentioned above, the eight (8) scrambling codes may be generated in parallel, using eight (8) separate scrambling code generators each operating independently, or the eight (8) scrambling codes maybe generated using a single scrambling code generator using eight (8) sets of masks (a set = 4 18-bit masks). However, both of these approaches require additional power consumption/silicon area. Under the first approach, additional scrambling code generators are needed; and under the second approach, additional memory storage is needed to store the received signals and it takes additional time to generate and process the necessary scrambling codes in a sequential manner.

[12] Hence, it would be desirable to provide a method and system which is capable of generating scrambling codes for correlation to identify a received scrambling code in a more efficient manner.

### SUMMARY OF THE INVENTION

[13] An exemplary method of the present invention is used to perform scrambling code detection of eight (8) primary cells (each scrambling code being spaced sixteen (16) chips apart) in a group. According to the exemplary method, a single scrambling code generator is used to generate a master scrambling code. The master scrambling code is then used to create individual scrambling codes which are used in correlation with received signals to detect in parallel which one of the eight (8) possible primary cells in the group transmitted the received signals. The individual scrambling codes are created based on the fact that each cell's phase reference is spaced sixteen (16) chips apart.

[14] The use of this exemplary method reduces the complexity of scrambling code or PN generator(s) in the parallel search implementation. More specifically, the use of this exemplary method avoids the need to utilize parallel logic to generate eight (8) scrambling codes. Since each primary scrambling code within a group is sixteen (16) chips apart, a buffer is used to store a sequential stream of scrambling code (i.e., the master scrambling code) output from a single scrambling code generator, and the received data is correlated in parallel with 16-chip offsets of portions of the master scrambling code. Eight dimensions are mapped to a single dimension at the expense of a slight increase in storage size.

[15] This exemplary method can be used as part of an overall 3-step initial cell search procedure to acquire the downlink of a 3GPP WCDMA cell, which more specifically corresponds to part of the stage 3 portion of the cell search procedure.

According to an exemplary implementation of the exemplary method of the present invention, the exemplary implementation includes a single scrambling code generator and eight (8) correlators arranged in sequential order. Each of the correlators maintains a corresponding segment of the master scrambling code. For every sixteen (16) chips, a new segment of the master scrambling code is introduced into one of the correlators by the scrambling code generator, a segment of the master scrambling code is dropped from another correlator, and segments of the master scrambling code are sequentially shifted or propagated through the remaining correlators; and concurrent correlations are performed by the correlators using their respective corresponding segments of the master scrambling code and newly received signals. The foregoing process is repeated until a desired correlation length is achieved.

[16] Reference to the remaining portions of the specification, including the drawings and claims, will realize other features and advantages of the present invention. Further features and advantages of the present invention, as well as the structure and operation of various embodiments of the present invention, are described in detail below with respect to accompanying drawings, like reference numbers indicate identical or functionally similar elements.

## BRIEF DESCRIPTION OF THE DRAWINGS

[17] Fig. 1 is a simplified diagram illustrating the timing of the scrambling codes of the eight (8) cells within a group;

[18] Fig. 2 is a flow diagram illustrating an exemplary method of the present invention;

[19] Fig. 3 is a simplified diagram illustrating parallel correlations of eight (8) cells in a group using a single scrambling code generator according to the present invention;

[20] Fig. 4 is a simplified diagram illustrating one exemplary implementation of the exemplary method according to the present invention;

[21] Fig. 5 is a flow diagram illustrating one exemplary implementation of the exemplary method in accordance with the present invention;

[22] Fig. 6 is a simplified diagram illustrating a second exemplary implementation of the exemplary method in accordance with the present invention;

[23] Fig. 7a is a diagram illustrating correlation results generated by the exemplary method shown in Fig. 3 in accordance with the present invention; and

[24] Fig. 7b is a diagram illustrating correlation results generated after one iteration by the exemplary implementation shown in Fig. 6 in accordance with the present invention.

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## DETAILED DESCRIPTION OF THE INVENTION

[25] The present invention in the form of one or more exemplary embodiments will now be discussed. The present invention can be applied to the third step of the initial cell search procedure when a mobile terminal is initially powered on to identify the base station or cell which transmitted the received signals containing a scrambling code. Fig. 1 is a simplified diagram illustrating the timing of the scrambling codes of the eight (8) cells within a group. Referring to Fig. 1, the scrambling code of each cell is transmitted on a periodic basis and the period of the scrambling code of each cell is thirty-eight thousand and four hundred (38,400) chips, i.e., the scrambling code of each cell is repeated after 38,400 chips. For example, for cell "0",  $C_0$  is transmitted at  $t_0$  and at  $t_{38,400}$ . Furthermore, the scrambling codes of any two adjacent cells are offset by sixteen (16) chips. For example, cells "0" and "1" transmit  $C_0$  and  $C_{16}$  respectively at  $t_0$ . The scrambling codes of all the cells within the group are transmitted at the same frame boundary. By having a 16-chip offset between two adjacent cells, the scrambling codes between two adjacent groups of cells are offset by one hundred and twenty-eight (128) ( $16 \times 8 = 128$ ).

[26] According to one exemplary method of the present invention, a scrambling code represented by the received signals is identified by using a single scrambling code generator to attain N chip correlation of the received signals with eight (8) primary scrambling codes in a group within  $N + 16 \times 7 = N + 112$  chips.

[27] Fig. 2 is a flow diagram illustrating an exemplary method of the present invention. Referring to Fig. 2, at 20, the correlation length N is first determined. The correlation length N is the amount of time during which correlation between the received signals and the generated scrambling codes is summed up. The correlation length N is selected such that reasonable correlation results can be obtained. Typical values of the correlation length N range from sixty-four (64) to two hundred and fifty-six (256), depending on the relative carrier frequency offset between the transmitted and received signals. A person of ordinary skill in the art will know how to select the proper correlation length. Next, at 22, using the selected correlation length, the chip offset (CO) between two adjacent scrambling codes, and the number of cells (C) within a group, a master scrambling code is

generated. The master scrambling code has a period, e.g., 38,400 chips, which is sufficient to allow correlations to be performed reliably.  $N+CO*(C-1)$  corresponds to the amount of code needed to be generated to perform a correlation of length  $N$  with  $C$  cells spaced  $CO$  chips apart. It should be noted that the product term  $CO*C$  represents the chip offset between the respective scrambling codes of the first cells of two adjacent groups of base stations or cells.

As mentioned above, during the first two steps of the initial cell search procedure, the start of the frame containing the scrambling code is identified and group identification information relating to the group which includes the cell that transmitted the received signals is available. With this information, the group which includes the cell that transmitted the received signals is identified. Moreover, using this information, the proper master scrambling code which covers all the possible scrambling codes from all the cells within the identified group can be generated. At 24, portions of the master scrambling code are used to create individual scrambling codes which correspond to the cells within the identified group. These individual scrambling codes are then correlated with the received signals in a parallel manner to determine which of the cells within the identified group transmitted the received signals.

[28] The following is an example illustrating the exemplary method of the present invention. The example is based on the following assumptions: the correlation length  $N$  is two hundred and fifty-six (256); the chip offset  $CO$  is sixteen (16); and the number of cells  $C$  within the identified group is eight (8). The period of the master scrambling code is thirty-eight thousand and four hundred (38,400) chips.

[29] Next, three hundred and sixty-eight (368) chips ( $C_0 \rightarrow C_{367}$ ) of master scrambling code is generated from a single scrambling code generator tuned to the first primary cell of the underlying identified group. The length of three hundred and sixty-eight (368) chips is determined based on the formula  $N+CO*(C-1)$  which, in this case, equals to  $256+16*(8-1) = 256+16*7 = 256+112 = 368$ . It should be noted that it is not necessary to generate all three hundred and sixty-eight (368) chips prior to correlation. The generation of three hundred and sixty-eight (368) chips is specified to emphasize the total number of chips required out of the scrambling code generator to implement eight (8) parallel correlations of two hundred and fifty-six (256) chips each.

[30] Fig. 3 is a simplified diagram illustrating parallel correlations of eight (8) cells in a group using a single scrambling code generator. As shown in Fig. 3, each of the eight (8) correlators correlates the received signals ( $D_0 \rightarrow D_{255}$ ) with two hundred and fifty-six (256) chips of scrambling code, each starting at an offset of sixteen (16) chips. For example, the first correlator correlates the received signals ( $D_0 \rightarrow D_{255}$ ) with the complex conjugate of ( $C_0$

→  $C_{255}$ ); the second correlator correlates the received signals ( $D_0 \rightarrow D_{255}$ ) with the complex conjugate of ( $C_{16} \rightarrow C_{271}$ ); and so on, and the final correlator correlates the received signals ( $D_0 \rightarrow D_{255}$ ) with the complex conjugate of ( $C_{112} \rightarrow C_{367}$ ). The correlation results are then obtained from each of the correlators. By evaluating the correlation results, the scrambling code represented by the received signals can be identified and, hence, the identity of the base station or cell which transmitted the received signals can also be determined.

[31] Fig. 4 is a simplified diagram illustrating an exemplary implementation of the exemplary method described above in accordance with the present invention. It is to be noted that the received signals are processed simultaneously in real-time by eight (8) parallel correlators. The scrambling code generator generates three hundred and sixty-eight (368), i.e.,  $N + 112$  chips. This is in contrast to  $8N$  chips that must be generated for the alternative approach in the parallel search implementation. Hence, there is a factor of  $8N/(N+128)$  savings on the scrambling code generation complexity using the present invention, which equals to 5.3 for  $N=256$  (an 82% reduction in complexity).

[32] The exemplary method of the present invention as described may be implemented in software, hardware or a combination of both. For example, the exemplary method of the present invention may be implemented as control logic using software embedded in a mobile terminal. When implemented using software, the exemplary method may be implemented in a modular or integrated manner within the mobile terminal. Based on disclosure provided herein, a person of ordinary skill in the art will know of other ways and/or methods to implement the present invention.

[33] Referring to Fig. 3, it can be seen that in accordance with the exemplary method, for a correlation length of two hundred and fifty-six (256), a 16-chip offset between scrambling codes and eight (8) cells within a group, a master scrambling code with a period of thirty-eight thousand and four hundred (38,400) chips is generated. This would require a global storage access of  $8N*2$  locations (since data is complex) for every  $N$  chips of correlation. If hardware resources are not limited, then the master scrambling code and the received signals can be stored in memory registers and each correlator can then read out the corresponding 256-chip scrambling code that it needs to perform the correlation.

[34] Fig. 5 is a flow diagram illustrating an exemplary implementation of the exemplary method described above in accordance with the present invention. As will be illustrated below, the exemplary implementation reduces the storage and access requirements needed to implement the exemplary method in accordance with the present invention. Referring to Fig. 5, at 50, a portion of the master scrambling code is generated to populate the



correlators. The generation of the master scrambling code by a single scrambling code generator is described above. The number of correlators and the length of each correlator respectively depend on the number of cells within a group and the chip offset between the respective scrambling codes of two adjacent cells within the group. The correlators collectively contain the entire generated portion of the master scrambling code, i.e., each correlator is populated with a segment of the generated portion of the master scrambling code. The length of the portion of the master scrambling code to be generated depends on the chip offset between the respective scrambling codes of two adjacent groups of cells. As mentioned above, this chip offset, in turn, depends on the number of cells within a group and the chip offset between the respective scrambling codes of two adjacent cells within the group. For instance, for a W-CDMA communication system, there are eight (8) cells in a group and the chip offset between the scrambling codes of two adjacent cells within the group is sixteen (16). Hence, the length of the portion of the master scrambling code to be initially generated is one hundred and twenty-eight ( $128 = 16 \times 8$ ) chips.

[35] At 52, a set of received signals are captured. The duration of the capture period equals to the chip offset between the respective scrambling codes of two adjacent cells within a group. For a W-CDMA communication system, the duration of the capture period is thus sixteen (16) chips.

[36] At 54, the set of received signals are correlated with the generated portion of the master scrambling code by the correlators and the correlation results are stored. After the correlations are performed, at 56, each correlator shifts or propagates its segment of the generated portion of the master scrambling code to its neighboring correlator, with the exception that, at 58, the first correlator discards its current segment and the last correlator receives a newly generated segment from the single scrambling code generator. Then, the process returns to 52 where the next set of received signals are captured and correlated. The foregoing process is repeated until the entire master scrambling code is generated and correlated. From an alternative perspective, this can be viewed as segments of the master scrambling code being correlated in a pipelined fashion on a first-in-first-out basis.

[37] Fig. 6 is an exemplary physical implementation of the exemplary method described above. Referring to Fig. 6, there is shown an exemplary system 60 having a single scrambling code generator 62 and eight (8) correlators 64-78. This exemplary system 60 operates based on the following assumptions: there are eight (8) cells in a group; the chip offset between the respective scrambling codes of two adjacent cells in a group is sixteen (16) chips; and the correlation length N is selected to be two hundred and fifty-six (256).

[38] The exemplary system 60 operates as follows. Initially, before any correlation is performed, the correlators  $COR_0$ - $COR_7$  64-78 are collectively populated with a portion of the master scrambling code by the single scrambling code generator 62. The portion of the master scrambling code that is initially generated is one hundred and twenty-eight ( $128 = 16 \times 8$ ) in length ( $C_0 \rightarrow C_{127}$ ). This portion of the master scrambling code is segmented and populated into the correlators  $COR_0$ - $COR_7$  64-78. Each correlator has a length of sixteen (16). For example, after initial population, correlator  $COR_0$  78 includes scrambling code segment  $C_0$ - $C_{15}$ ; correlator  $COR_1$  76 includes segment  $C_{16}$ - $C_{31}$ ; and correlator  $COR_7$  64 includes segment  $C_{112}$ - $C_{127}$ ; and so on. It should be noted that the initial generation of all one hundred and twenty-eight (128) chips of the portion of the master scrambling code before starting any of the correlations is not required. One of the correlators 64-78 can be started every sixteen (16) chips in a pipelined fashion.

[39] Next, a set of complex data signals or samples which is sixteen (16) chips in length,  $D_0$ - $D_{15}$ , are received and fed to each of the correlators 64-78. Then, each correlator partially correlates the same set of received complex data samples with the complex conjugate of its corresponding scrambling code segment. It is to be noted that the respective scrambling code segments of any two adjacent correlators have a chip offset of sixteen (16) chips. For example, the first correlator  $COR_0$  78 correlates the received data samples,  $D_0$ - $D_{15}$ , with the complex conjugate of its corresponding scrambling code segment,  $C_0$ - $C_{15}$ ; the second correlator  $COR_1$  76 correlates the received data samples,  $D_0$ - $D_{15}$ , with the complex conjugate of its corresponding scrambling code segment,  $C_{16}$ - $C_{31}$ ; and so on, and the final correlator  $COR_7$  64 correlates the received data samples,  $D_0$ - $D_{15}$ , with the complex conjugate of its corresponding scrambling code segment,  $C_{112}$ - $C_{127}$ . The correlations of the eight (8) correlators 64-78 are performed concurrently in a parallel manner and the correlation results are stored for subsequent evaluation.

[40] After this first iteration of correlations, each correlator passes its current corresponding scrambling code segment to a neighboring correlator. It should be noted that each correlator has two neighboring correlators. In effect, with two exceptions which will be described below, this means each correlator also receives a new corresponding scramble code segment from another neighboring correlator. Graphically, this is shown as follows:  $COR_1 \rightarrow COR_0$ ,  $COR_2 \rightarrow COR_1$ ,  $COR_3 \rightarrow COR_2$ ,  $COR_4 \rightarrow COR_3$ ,  $COR_5 \rightarrow COR_4$ ,  $COR_6 \rightarrow COR_5$ . In essence, the scrambling code segments are shifted or propagated along the correlators 64-78. The two exceptions are the first correlator  $COR_0$  78 and the last correlator  $COR_7$  64. For the first correlator  $COR_0$  78, its current corresponding scramble code segment

is discarded; and for the last correlator  $COR_7$  64, a new scrambling code segment generated by the single scrambling code generator 62 is fed to the last correlator  $COR_7$  64. The newly generated scrambling code segment is the next segment of the master scrambling code that follows the scrambling code segment that was in the last correlator  $COR_7$  64 before that scrambling code segment was transferred to correlator  $COR_6$  66. For example, after the first iteration, the newly generated scrambling code segment to be fed into the last correlator  $COR_7$  64 is  $C_{128}-C_{143}$ . This is because scrambling code segment  $C_{128}-C_{143}$  follows scrambling code segment  $C_{112}-C_{127}$  within the master scrambling code. By shifting or transferring the scrambling code segments as described above, the entire master scrambling code is propagated along all the correlators 64-78 and is eventually correlated with the received data samples as described in Fig. 3.

[41] The next set of complex data samples are received,  $D_{16}-D_{31}$ , and loaded into the correlators 64-78. Another iteration of concurrent correlations by the correlators 64-78 is then performed again. The foregoing process of shifting the scrambling code segments, receiving the next set of complex data samples and performing another iteration of correlations is repeated until the master scrambling code is correlated or, conversely, the collective length of all the received complex data samples reaches the correlation length.

[42] Fig. 7a illustrates the correlation results generated using the exemplary method shown in Fig. 3. Fig. 7b illustrates the correlation results after the first iteration of correlations by the exemplary system 60. Comparing Figs. 7a and 7b, it can be seen that by using the exemplary system 60 shown in Fig. 6, sixteen (16) terms are generated by each of the eight (8) correlations after one iteration. Hence, in order to generate the complete results as shown in Fig. 7a based on a correlation length of two hundred and fifty-six (256), sixteen (16) total iterations are executed.

[43] As can be seen above, scrambling code segments which make up the master scrambling code are internally shared amongst correlators 64-78. For every predetermined period (that is defined by the chip offset between the respective scrambling codes of any two adjacent cells within a group), e.g., sixteen (16) chips, the corresponding scrambling code segment of each correlator is refreshed or updated. This sharing of scrambling code segment reduces the access to the otherwise globally stored master scrambling code by a factor of eight (8) (for cases using the above assumptions), i.e.,  $2N$  scrambling code read accesses are required every  $N$  chips of correlations.

[44] In addition, for every iteration of correlations, the scrambling code generator 62 transfers or generates only  $2N/16$  binary values to one of the correlators 64-78. As

mentioned above, the rest of the correlators 66-78 internally share the scrambling code segments which are already present amongst the correlators 64-78.

[45] To further reduce scrambling code memory access as well as each correlator's working size, the technique of packing and unpacking bits may be used. If the above binary values were packed into a word and then unpacked at the time of correlation, the scrambling code generator 62 then needs to transfer only  $2N/(16*16)$  16-bit words to the group of correlators 64-78. That is, the working size of each correlator may be reduced to  $2N/(16*16)$  by packing sixteen (16) bits at a time. This also reduces scrambling code storage access to  $2N/16$ .

[46] Furthermore, it is understood that while the present invention as described above is applicable to a W-CDMA communication system, it should be clear to a person of ordinary skill in the art that the present invention can be applied to other types of communication systems.

[47] Moreover, it should be noted that the present invention as described herein may be implemented in a number of ways. For example, the present invention may be implemented using the adaptive computing architecture as disclosed in U.S. patent application serial no. 09/815,122 entitled "ADAPTIVE INTEGRATED CIRCUITRY WITH HETEROGENEOUS AND RECONFIGURABLE MATRICES OF DIVERSE AND ADAPTIVE COMPUTATIONAL UNITS HAVING FIXED, APPLICATION SPECIFIC COMPUTATIONAL ELEMENTS," filed on March 22, 2001, the disclosure of which is hereby incorporated by reference in their entirety as if set forth in full herein for all purposes. For instance, using the adaptive computing architecture, the scrambling code generator 62 and the correlators 64-78 may be implemented on demand within a mobile terminal. Based on the disclosure provided herein, a person of ordinary skill in the art will know of other ways and/or methods to implement and apply the present invention.

[48] It is further understood that the examples and embodiments described herein are for illustrative purposes only and that various modifications or changes in light thereof will be suggested to persons skilled in the art and are to be included within the spirit and purview of this application and scope of the appended claims. All publications, patents, and patent applications cited herein are hereby incorporated by reference for all purposes in their entirety.